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PATENT
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Ohtani, et al.
Serial No.: 09/006,844
Filed : December 29, 1997
Title : SEMICONDUCTOR DEVICE AND METHOD OF MANUFACTURING THE SAME

Art Unit:
Examiner:

Commissioner of Patents and Trademarks
Washington, DC 20231

TRANSMITTAL OF PRIORITY DOCUMENT(S) UNDER 35 U.S.C. 119

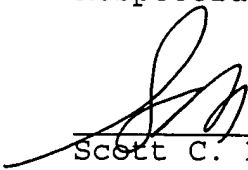
Applicants hereby confirm their claim of priority under 35 U.S.C. 119 from Japanese Application Nos. 9-022077 filed January 20, 1997, and 9-123088 filed April 26, 1997. Certified copies of the applications from which priority is claimed are submitted herewith.

Please charge any fees due in this respect to Deposit Account No. 06-1050.

Respectfully submitted,

Date:

3/10/98

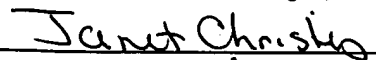


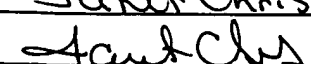
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I hereby certify under 37 CFR 1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated above and is addressed to the Assistant Commissioner for Patents, Washington, D.C. 20231.





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る事項と同一であることを証明する。

This is to certify that the annexed is a true copy of the following application as filed
in this Office.

願 年 月 日
Date of Application:

1997年 1月20日

願 番 号
Application Number:

平成 9年特許願第022077号

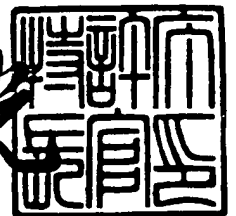
願 人
Applicant(s):

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1997年11月28日

特許庁長官
Commissioner,
Patent Office

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